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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Rajski et al.

Filed: February 10, 2004 Confirmation No. 4800

For: ARITHMETIC BUILT-IN SELF-TEST OF

MULTIPLE SCAN-BASED INTEGRATED

CIRCUITS

Examiner: Norman M. Wright

Art Unit: 2134

Attorney Reference No. 1011-67625-01

MAIL STOP AMENDMENT COMMISSIONER FOR PATENTS P.O. BOX 1450 ALEXANDRIA, VA 22313-1450

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Attorney for Applicant(s)

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Date Mailed _____

AMENDMENT

This responds to the Office action dated October 6, 2004. Please amend the referenced application as follows:

Amendments to the Claims are reflected in the listing of claims, which begins on page 2.

Remarks begin on page 7.

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